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**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q76864

Naoki MATSUNAGA, et al.

Appln. No.: 10/724,040

Group Art Unit: 2818

Confirmation No.: 1459

Examiner: Dao H. NGUYEN

Filed: December 01, 2003

For: THIN FILM TRANSISTOR, TFT SUBSTRATE AND LIQUID CRYSTAL DISPLAY  
UNIT

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 26, 2005, in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-19. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,



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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: October 25, 2005